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FIG.1



FIG.2



FIG.3



FIG.4

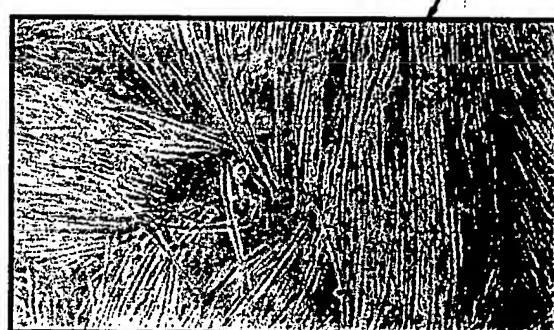
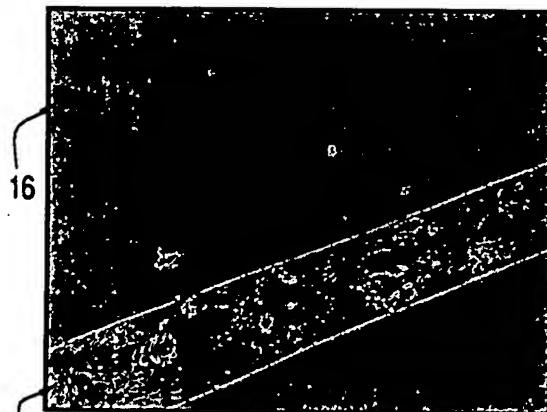
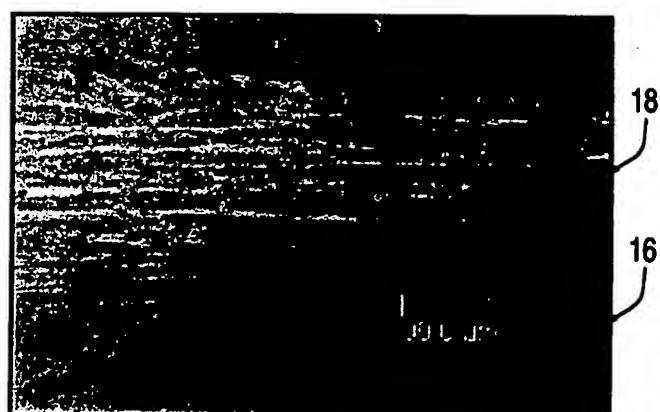


FIG.5



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FIG.6

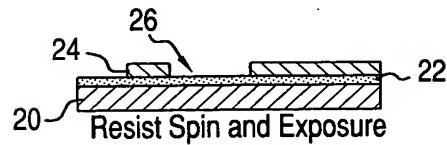
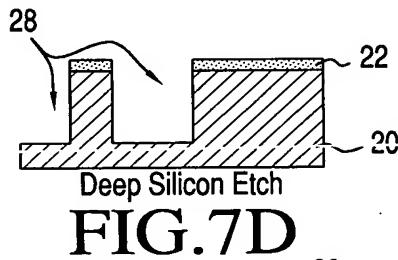
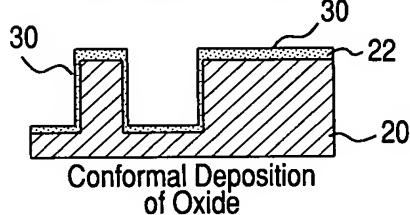
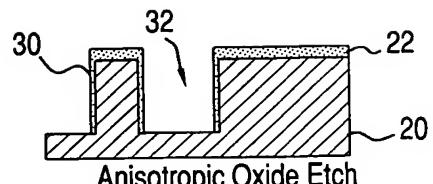
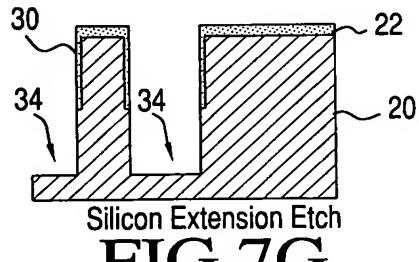
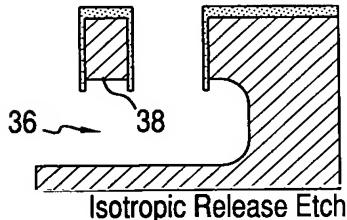
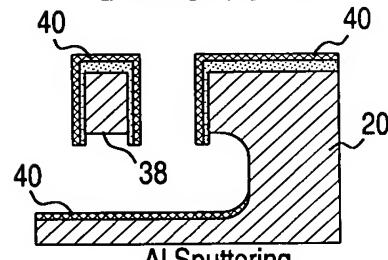
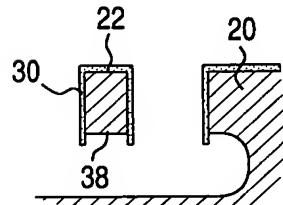
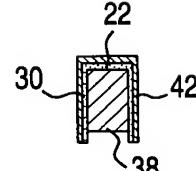
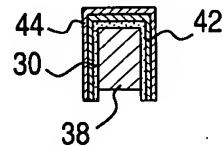
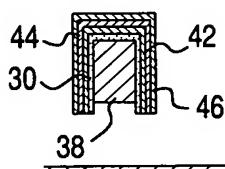
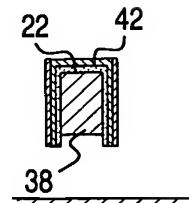


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**FIG.7A****FIG.7B****FIG.7C****FIG.7D****FIG.7E****FIG.7F****FIG.7G****FIG.7H****FIG.7I****FIG.8A****FIG.8B****FIG.8C****FIG.8D****FIG.8E**



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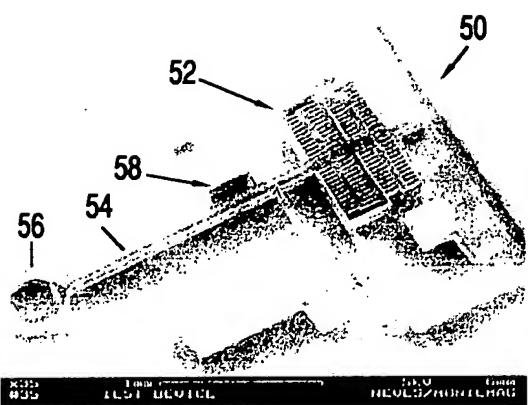


FIG.9A

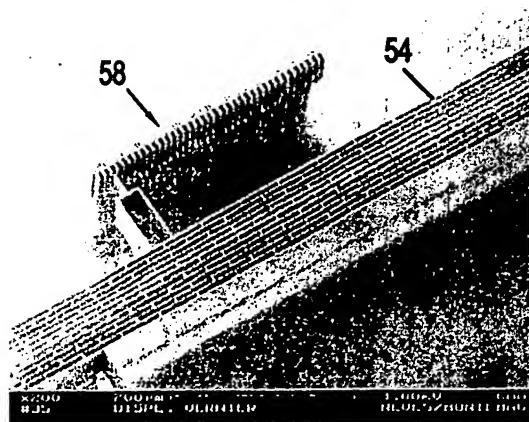


FIG.9B

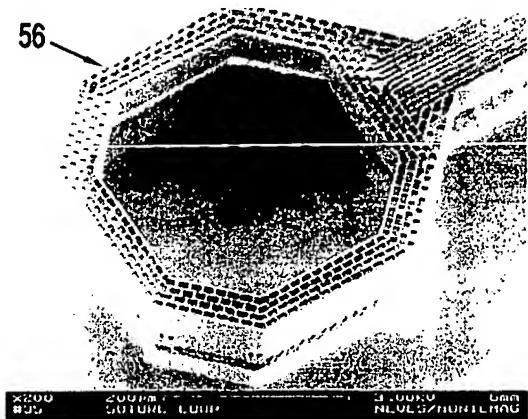


FIG.9C

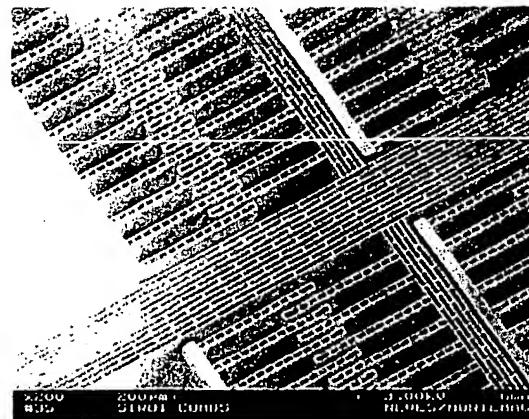


FIG.9D

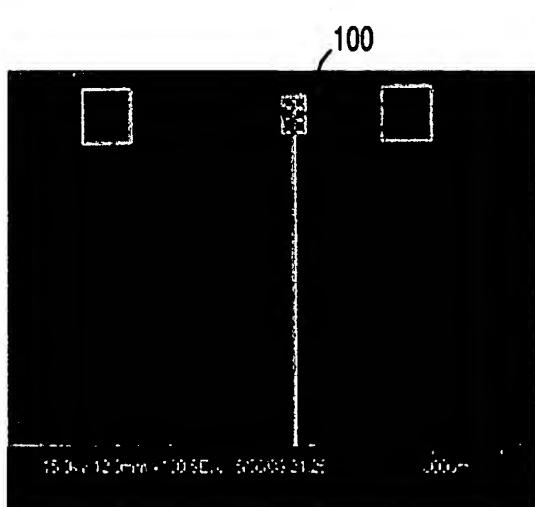


FIG.12A

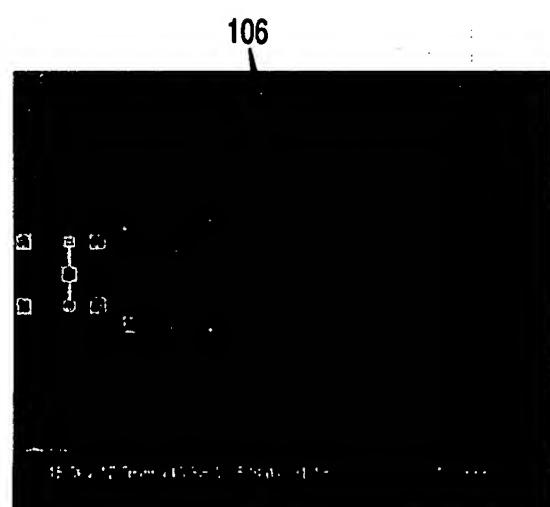


FIG.12B



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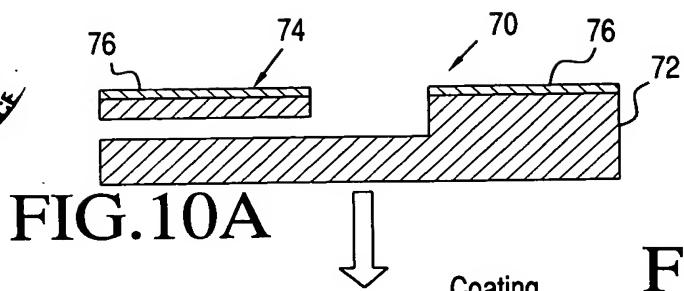


FIG. 10A

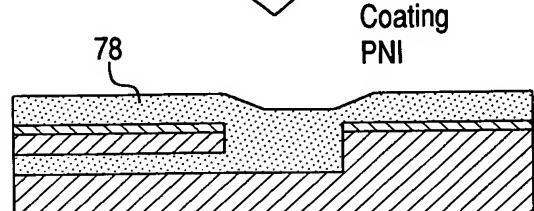


FIG. 10B

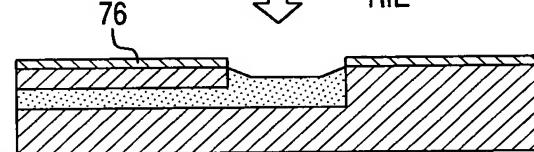


FIG. 10C

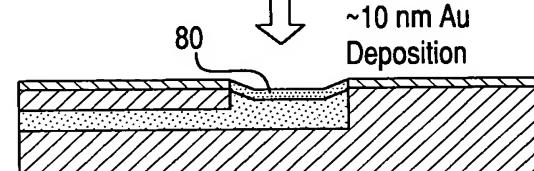


FIG. 10D

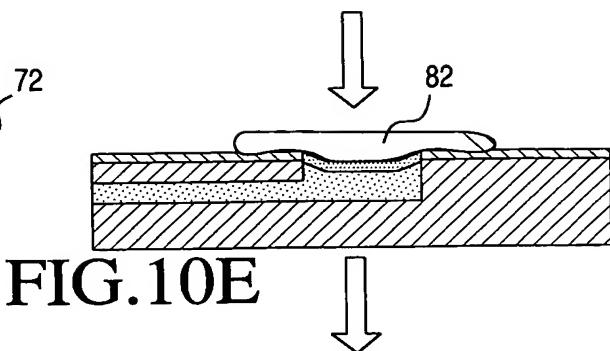


FIG. 10E

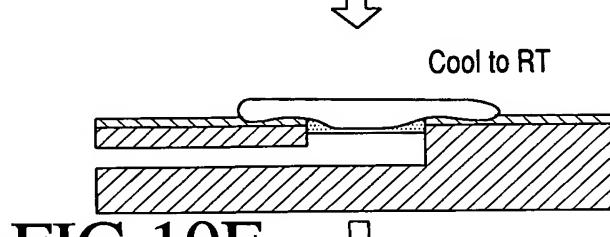


FIG. 10F

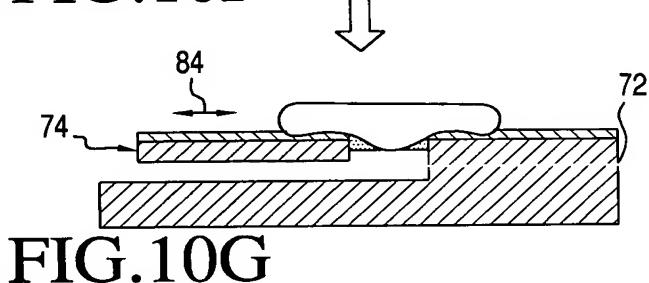


FIG. 10G

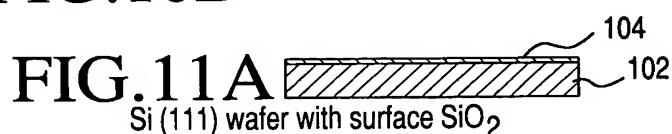


FIG. 11A Si (111) wafer with surface SiO_2

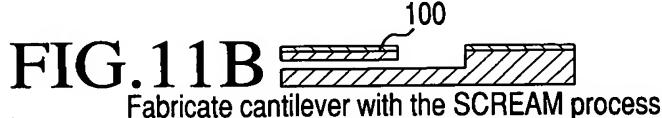


FIG. 11B Fabricate cantilever with the SCREAM process

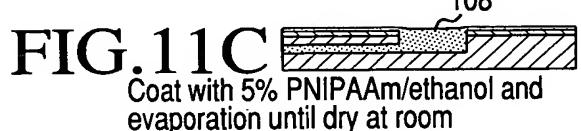


FIG. 11C Coat with 5% PNIPAAm/ethanol and evaporation until dry at room



FIG. 11D Etch PNIPAAm through a shadow mask

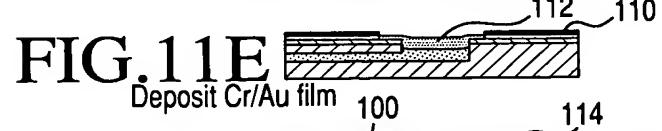


FIG. 11E Deposit Cr/Au film



FIG. 11F Remove shadow mask

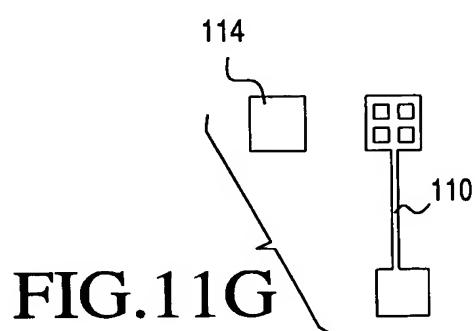


FIG. 11G

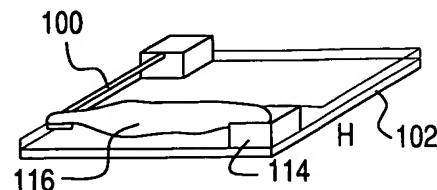


FIG. 11H



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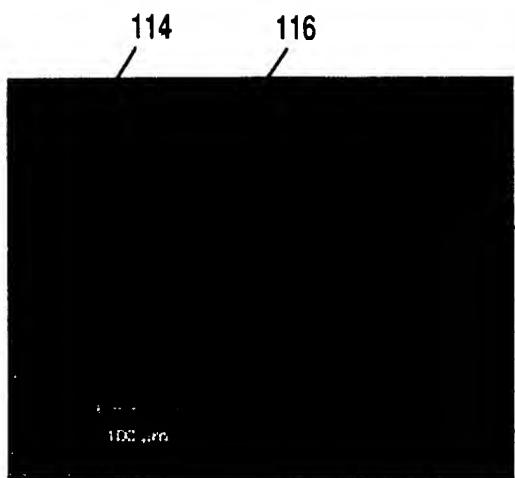


FIG.13A

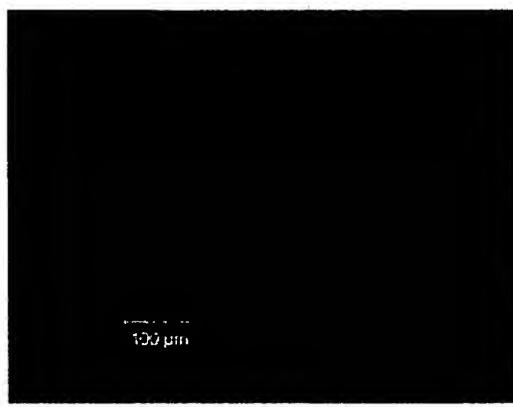


FIG.13B

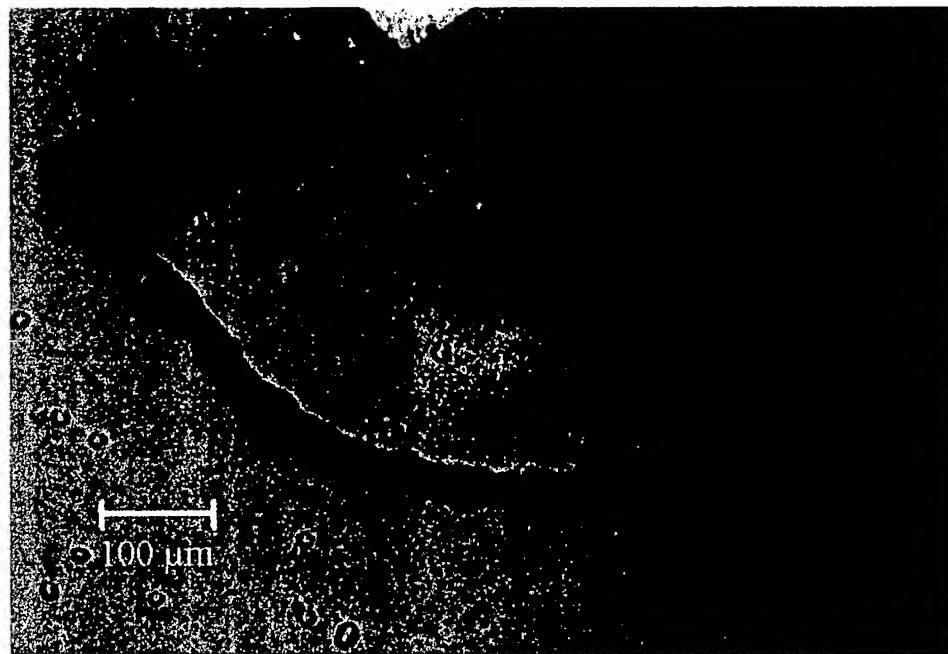


FIG.14